Express Mail Label No.: EV062336985US Attorney Docket No.: N1085-00127 [TSMC 2002-0989]

ABSTRACT

A printed circuit board has a circuit trace on it and a solder mask over the circuit trace. The solder mask is removed from the printed circuit board using an ultra violet laser, to expose the circuit trace without damaging the circuit trace. A failure analysis is performed on the circuit trace of the printed circuit board.

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